

# 2MBI150VH-170-50

**IGBT Modules** 

# **IGBT MODULE (V series)** 1700V / 150A / 2 in one package

#### Features

High speed switching Voltage drive Low Inductance module structure

#### Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as Welding machines

## ■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings (at T<sub>c</sub>=25°C unless otherwise specified)



Items	Symbols	Conditions	Conditions		Units	
Collector-Emitter voltage	Vces			1700	V	
Gate-Emitter voltage	V <sub>GES</sub>			±20	V	
Collector current	Ic	Continuous	Tc=25°C	150		
		Continuous	Tc=100°C	250		
	I <sub>C pulse</sub>	1ms		300	Α	
	-Ic			150		
	-I <sub>C pulse</sub>	1ms	,	300		
Collector power dissipation	Pc	1 device		1110	W	
Junction temperature	Tj			175	°C	
Operating junction temperature (under switching conditions)	Tjop			150		
Case temperature	Tc			125		
Storage temperature	T <sub>stg</sub>					
Isolation voltage   between terminal and copper base (*1)	Viso	AC: 1min.		4000	VAC	
Screw torque Mounting (*2)	-			6.0	N m	
Terminals (*3)	-			5.0	IN (II	

Note \*1: All terminals should be connected together during the test. Note \*2: Recommendable Value :  $3.0\text{-}6.0~\text{N}\cdot\text{m}$  (M5 or M6) Note \*3: Recommendable Value :  $2.5\text{-}5.0~\text{N}\cdot\text{m}$  (M5)

### Electrical characteristics (at T<sub>i</sub>= 25°C unless otherwise specified)

Itomo	Symbolo	Conditions		Characteristics			Heite
Items	Symbols			min.	typ.	max.	Units
Zero gate voltage collector current	Ices	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1700V		-	-	2.0	mA
Gate-Emitter leakage current	IGES	$V_{CE} = 0V$ , $V_{GE} = \pm 20V$		-	-	400	nA
Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 150mA		6.0	6.5	7.0	V
Collector-Emitter saturation voltage	V <sub>CE</sub> (sat)	V <sub>GE</sub> = 15V I <sub>C</sub> = 150A	T <sub>j</sub> =25°C	-	2.20	2.65	V
	(terminal)		T <sub>j</sub> =125°C	-	2.60	-	
	(terrillial)		T <sub>j</sub> =150°C	-	2.65	-	
	V		T <sub>j</sub> =25°C	-	2.00	2.45	
	V <sub>CE</sub> (sat)		T <sub>j</sub> =125°C	-	2.40	-	
	(chip)		T <sub>j</sub> =150°C	-	2.45	-	
Internal gate resistance	R <sub>G (int)</sub>	-		-	5.0	-	Ω
Input capacitance	Cies	$V_{CE} = 10V$ , $V_{GE} = 0V$ , $f = 1MHz$		-	16	-	nF
Turn-on time	ton	V <sub>cc</sub> = 900V, I <sub>c</sub> = 150A V <sub>GE</sub> = ±15V, Rg_on=Rg_off= 4.8Ω T <sub>j</sub> =150°C, L <sub>s</sub> = 30nH		-	950	-	nsec
	tr			-	350	-	
	t <sub>r (i)</sub>			-	60	-	
Turn-off time	toff			-	1050	-	
	t <sub>f</sub>			-	140	-	
Forward on voltage	VF	V <sub>GE</sub> = 0V I <sub>F</sub> = 150A	T <sub>j</sub> =25°C	-	1.95	2.40	V
	(terminal)		T <sub>j</sub> =125°C	-	2.20	-	
	(terminar)		T <sub>j</sub> =150°C	-	2.20	-	
	VF		T <sub>j</sub> =25°C	-	1.80	1.95	
	1		T <sub>j</sub> =125°C	-	2.05	-	
	(chip)		T <sub>j</sub> =150°C	-	2.05	-	
Reverse recovery time	trr	I <sub>F</sub> = 150A		-	220	-	nsec

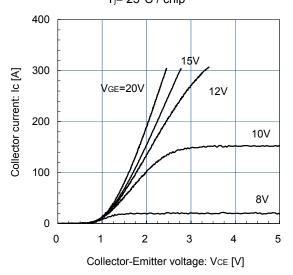
#### Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units	
items		Conditions	min.	typ.	max.	Ullits	
Thermal resistance(1device)	R <sub>th(j-c)</sub>	IGBT	-	-	0.135	°C/W	
		FWD	-	-	0.200		
Contact thermal resistance (1device) (*4)	R <sub>th(c-f)</sub>	with Thermal Compound	-	0.0250	-		

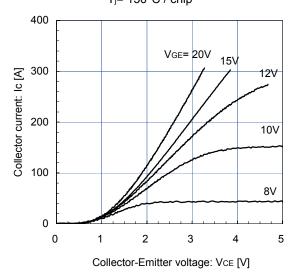
Note \*4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

#### **■** Characteristics (Representative)

Collector current vs. Collector-Emitter voltage (typ.)  $T_j = 25^{\circ}C$  / chip

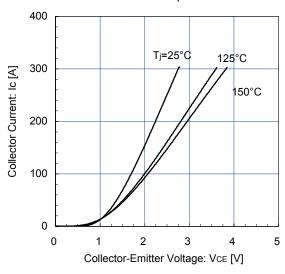


Collector current vs. Collector-Emitter voltage (typ.)  $T_j$ = 150°C / chip

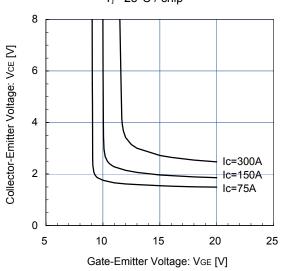


Collector current vs. Collector-Emitter voltage (typ.)

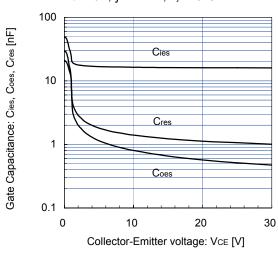
VGE= 15V / chip



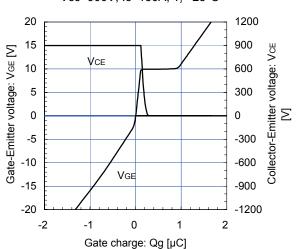
Collector-Emitter voltage vs. Gate-Emitter voltage T<sub>i</sub>= 25°C / chip



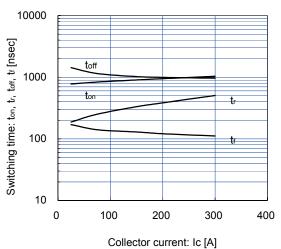
Gate Capacitance vs. Collector-Emitter Voltage VGE= 0V, f= 1MHz,  $T_j$ = 25°C



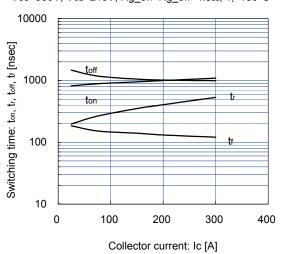
Dynamic Gate Charge (typ.) Vcc=900V, Ic=150A, T<sub>j</sub>= 25°C



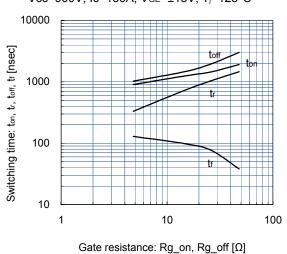
Switching time vs. Collector current (typ.) Vcc=900V, V $_{GE}$ =±15V, Rg\_on=Rg\_off=4.8 $\Omega$ , T $_{j}$ =125 $^{\circ}$ C



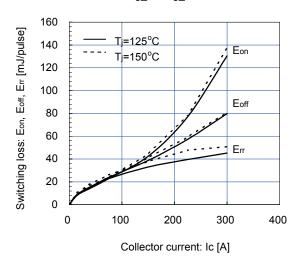
Switching time vs. Collector current (typ.) Vcc=900V, V $_{GE}$ =±15V, Rg\_on=Rg\_off=4.8 $\Omega$ , T $_{j}$ =150°C



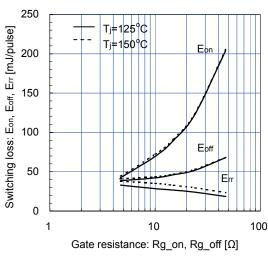
Switching time vs. Gate resistance (typ.) Vcc=900V, Ic=150A, VgE=±15V, T<sub>i</sub>=125°C



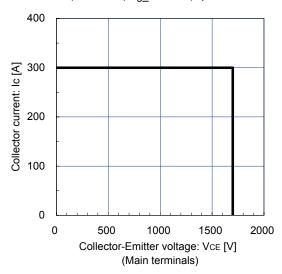
Switching loss vs. Collector current (typ.) Vcc=900V, VgE=±15V, Rg\_on=Rg\_off=4.8Ω, Tj=125, 150°C



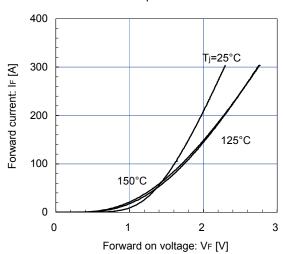
Switching loss vs. Gate resistance (typ.) Vcc=900V, Ic=150A,  $V_{\text{GE}}$ =±15V,  $T_{\text{j}}$ =125, 150°C



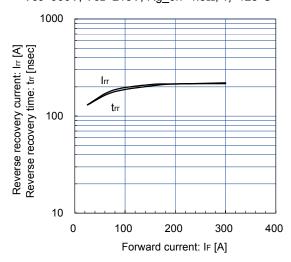
Reverse bias safe operating area (max.) +V<sub>GE</sub>=15V, -V<sub>GE</sub>=15V, Rg\_off=4.8 $\Omega$ , T<sub>j</sub>=150°C



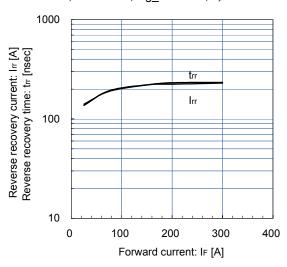
Forward Current vs. Forward Voltage (typ.) chip



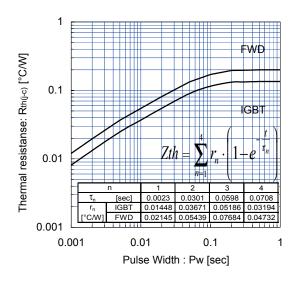
Reverse Recovery Characteristics (typ.) Vcc=900V, VgE=±15V, Rg\_on=4.8Ω, Tj=125°C



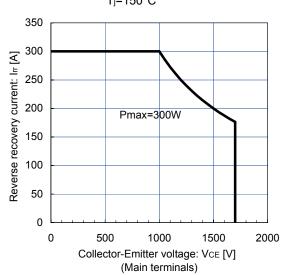
Reverse Recovery Characteristics (typ.) Vcc=900V, VgE=±15V, Rg on=4.8Ω, Tj=150°C



Transient Thermal Resistance (max.)

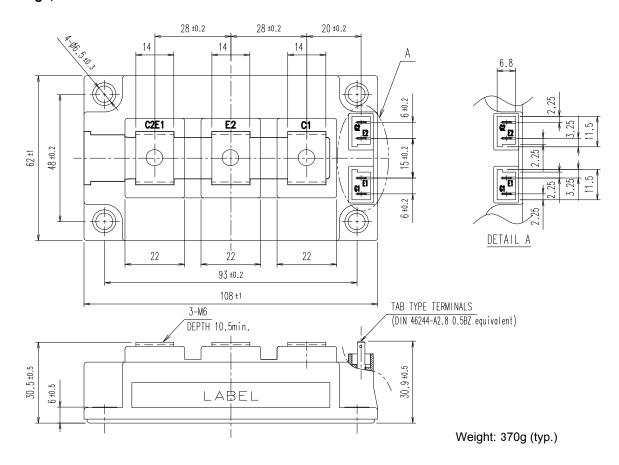


FWD safe operating area (max.)  $T_i=150$ °C

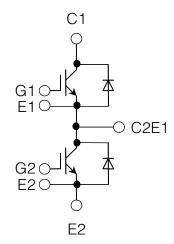


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# ■ Outline Drawings, mm



# **■** Equivalent Circuit Schematic



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